

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: ) Confirmation No. 6856  
Shunpei YAMAZAKI et al. ) Group Art Unit: 2894  
Serial No. 10/583,365 ) Examiner: Alexander Belousov  
Filed: June 19, 2006 )  
For: SEMICONDUCTOR DEVICE )

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. §1.56 and 37 C.F.R. §§1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. §1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

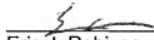
The references listed on the attached Form PTO 1449 were cited in Japanese counterpart Patent Application Serial No. JP 2005-063905 dated March 23, 2010.

Although no fee is due for the submission of this Information Disclosure Statement, the Commissioner is hereby authorized to charge fees under 37 C.F.R. §§

1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,

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